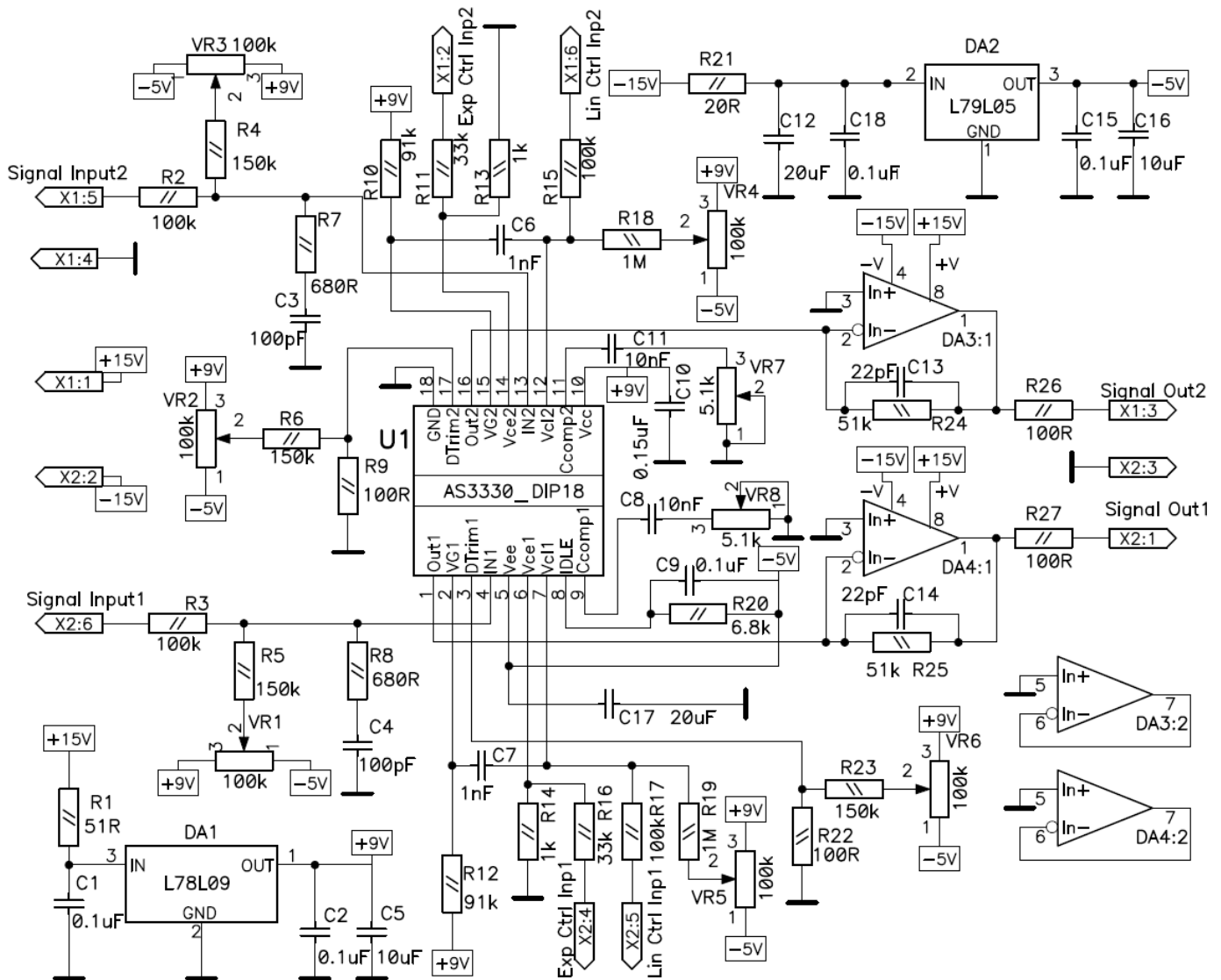


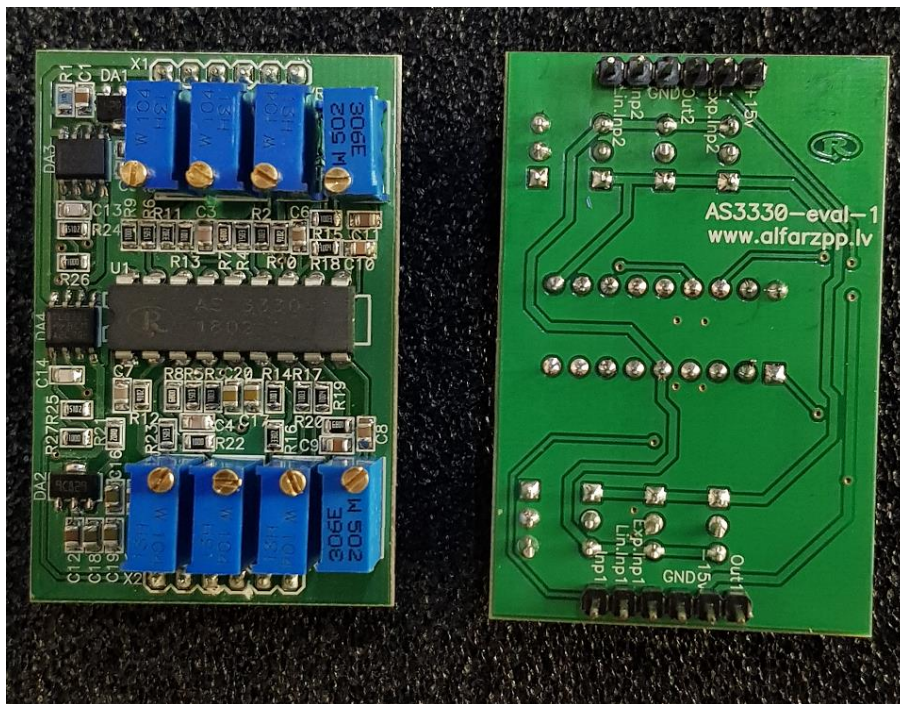
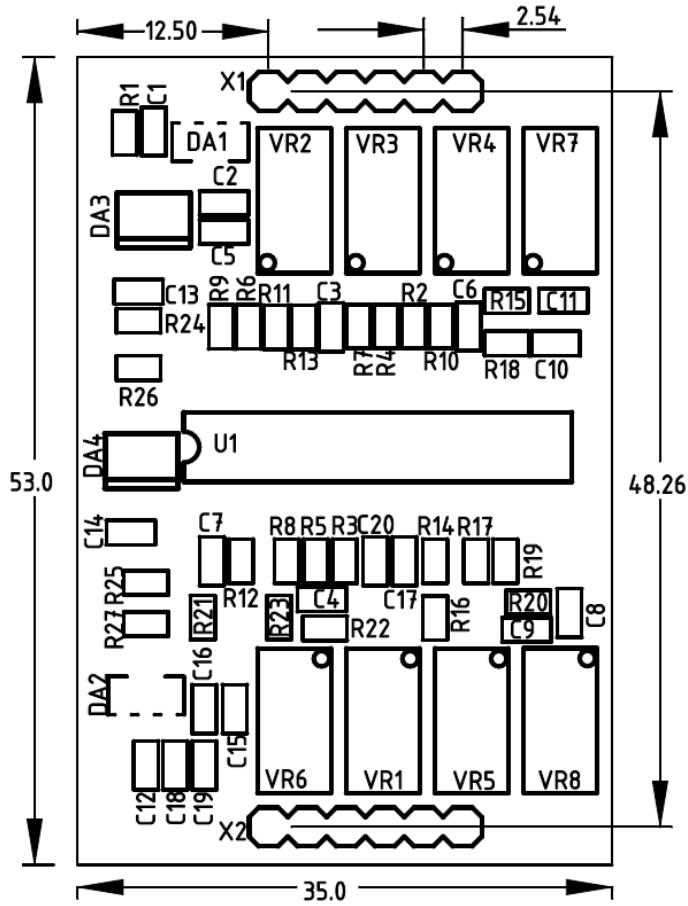
**AS3330-app 1 (application note)**

**Schematic Diagram**



**Notes:**

1. VR1, VR3 - trimming input offset IN1 and IN2
2. VR6, VR2 - trimming THD on output. Full symmetry of amplification must be achieved for positive and negative signals.  
 At first negative signal (-9 V) must be applied to input and output voltage must be noted. After that positive signal (+9V) must be applied and the same output voltage (but with different sign) must be achieved turning VR6, VR2. As voltage source - battery +9V may be used (just changing polarity).
3. VR7, VR8 – optimal resistance for minimum HF noise and minimum feedthrough of control signals. Compromise resistors are 2.5k. But for optimal trimming better to use potentiometers.
4. Capacitor C17 must be as close as possible to Pin 5 (Vee)
5. To minimize crosstalk between channels two TL072 were used.
6. Operating voltage : +Vcc = +15V / + 12V ; -Vee = -15V / -12V



**Revision history**

Date	Revision	Changes
04-Mar-2020	1	Preliminary version 1
23-Mar-2020	1	Photo of module added