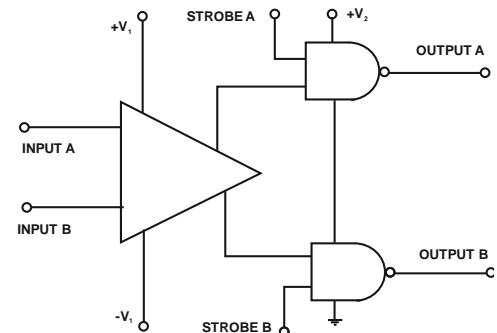




Voltage comparator

Block diagram AS527



FEATURES

Pin-to-pin compatible with industry standard NE527
15 ns propagation delay
Complementary output gates
TTL or ECL compatible outputs
Wide common-mode and differential voltage range
Typical gain of 5000

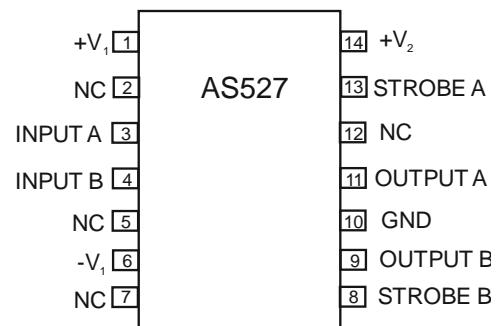
DESCRIPTION

AS527 is a high-speed analog voltage comparator which mates Schottky diode technology with the conventional linear process. This allows simultaneous fabrication of high speed TTL gates with a precision linear amplifier on a single monolithic chip.

APPLICATIONS

A/D conversion
ECL-to-TTL interface
TTL-to-ECL interface
Memory sensing
Optical data coupling

CONNECTION DIAGRAM SOIC-14 Package



ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
+V ₁	Positive supply voltage	+15	V
-V ₁	Negative supply voltage	-15	V
+V ₂	Gate supply voltage	+7	V
V _{OUT}	Output voltage	+7	V
V _{IN}	Differential input voltage	+5	V
P _{MAX}	Max power dissipation ¹ 25 °C ambient (still air)	1000	mW
T _{amb}	Operating temperature range	0 to +70	°C
T _{stg}	Storage temperature range	-65 to +150	°C
T _{sld}	Lead soldering temperature (10sec max)	+230	°C

NOTE: 1. Derate above 25 °C, at the following rates 8.3 mW/°C



DC ELECTRICAL CHARACTERISTICS

$V_{1+} = 10V$; $V_{1-} = -10 V$; $V_{2+} = +5.0 V$; unless otherwise specified.

SYMBOL	PARAMETR	Test conditions	AS527		UNIT
			Min	Max	
Input characteristics					
V_{OS}	Input offset voltage @ 25 over temperature range			5	mV
				10	
I_{BIAS}	Input bias current @ 25 °C over temperature range			2	μA
				4	
I_{OS}	Input offset current @ 25 °C over temperature range	$V_{IN} = 0 V$		0,75	μA
				1	
V_{CM}	Common-mode voltage range		-5	+5	V
Gate characteristics					
V_{OUT}	Output Voltage "1" State "0" State	$V_{2+} = 4.75 V$; $I_{SOURCE} = -1 mA$ $V_{2+} = 4.75 V$; $I_{SINK} = 10 mA$		2,9	V
				0,49	
	Strobe inputs "0" Input current ¹ "1" Input current @ 25 °C ¹ Over temperature range "0" Input voltage "1" Input voltage	$V_{2+} = 5.25 V$; $V_{STROBE} = 0.5 V$ $V_{2+} = 5.25 V$; $V_{STROBE} = 2,7 V$ $V_{2+} = 5.25 V$; $V_{STROBE} = 2,7 V$ $V_{2+} = 4.75 V$ $V_{2+} = 4.75 V$			
				-2	mA
				120	μA
				200	μA
				0,8	V
				2,0	V
I_{SC}	Short-circuit output current	$V_{2+} = 5.25 V$; $V_{OUT} = 0 V$	-18	-70	mA
Power supply requirements					
V_{1+} V_{1-} V_{2+}	Supply voltage				
			5	10	V
			-6	-10	V
			4,75	5,25	V
I_{1+} I_{1-} I_{2+}	Supply current	$V_{1+} = 10 V$; $V_{1-} = -10 V$; $V_{2+} = 5.25 V$ Over temp. Over temp. Over temp.			
				3,6	mA
				7	mA
				14	mA

NOTE: 1. See Logic Function Table.

AC ELECTRICAL CHARACTERISTICS

$T_{AMB} = 25 ^\circ C$, unless otherwise specified.

SYMBOL	PARAMETR	Test conditions	AS527		UNIT
			Min	Max	
t_{PLH}	Transient response propagation delay time Low-to-High High-to-Low	$V_{IN} = \pm 100 mV$ step			
				26	ns
				24	ns
	Delay between output A and B			5	ns
t_{ON} t_{OFF}	Strobe delay time Turn-on time Turn-off time				
				6(Typ)	ns
				6(Typ)	ns

LOGIC FUNCTION

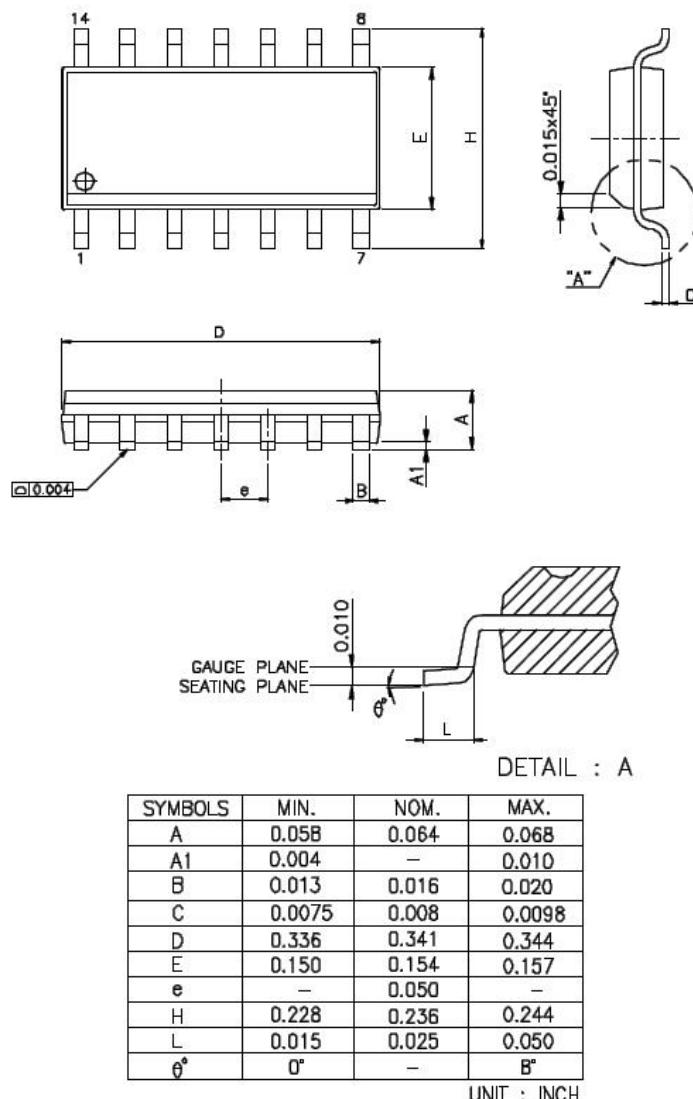
$V_{ID}(A^+, B^-)$	STROBE A	STROBE B	OUTPUT A	OUTPUT B	COMMENT
$V_{ID} \leq -V_{OS}$	H	X	L	H	Read I_{IHA} , I_{ILB}
$-V_{OS} < V_{ID} < V_{OS}$	H	H	Undefined	Undefined	
$V_{ID} \geq V_{OS}$	X	H	H	L	Read I_{ILA} , I_{IHB}
X	L	L	H	H	



AS "ALFA RPAR"

AS527

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SOIC-14